Partner: Planar
Model: Matrix
Device Type: LCD Display

CRESTRON
CERTIFIED INTEGRATED PARTNER

GENERAL INFORMATION

SIMPLWINDOWS NAME:	Planar Matrix v1.0
CATEGORY:	TV/Display Device
VERSION:	1.0
SUMMARY:	Controls Planar Clarity Matrix video wall
GENERAL NOTES:	
CRESTRON HARDWARE REQUIRED:	2-Series Processor with COM port, 3-Series Processor with COM port
COM SETUP:	RS-232 Baud: 19200 Parity: None Data Bits: 8 Stop Bits: 1
VENDOR FIRMWARE:	N/A
VENDOR SETUP:	N/A
CABLE DIAGRAM:	CNSP-121

CNSP-121

Partner: Planar
Model: Matrix
Device Type: LCD Display

CRESTRON
CERTIFIED **INTEGRATED** PARTNER
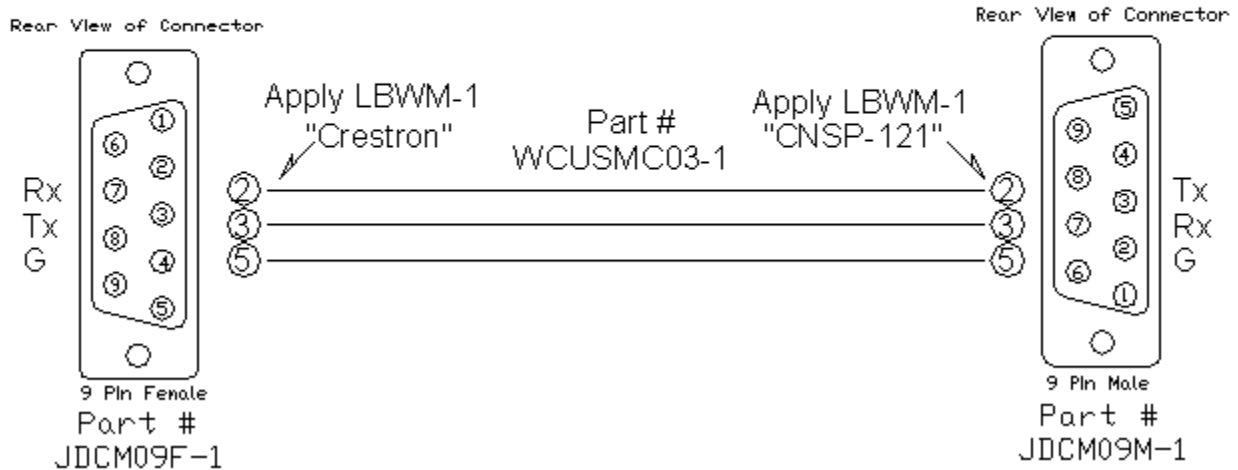
CONTROL:

Device_tx	S	Serial data to com port
Device_rx	S	Serial data from com port
Power_on	D	Pulse to turn power on
Power_off	D	Pulse to turn power off
Memory_store	D	Pulse to enter store memory slot mode
Memory_recall	D	Pulse to enter recall memory slot mode
Memory_slot	A	Sets the memory slot index value to be recalled or stored (range is 1d-40d)
Backlight_intensity	A	Sets the intensity of the backlight for all displays (range is 1d-10d)
Remote_control_on	D	Pulse to enable the infrared receiver
Remote_control_off	D	Pulse to disable the infrared receiver
Key_XXX	D	Pulse to emulate remote keys
Individual	S	Serial data from 'individual' control modules
Initialize	D	Pulse to initialize communications
Update	D	Pulse to update feedback (optional)

FEEDBACK:

System-state_XXX(fb)	D	Indicates the current system state
Memory_store(active)	D	Indicates that the memory slot store mode is active
Memory_recall(active)	D	Indicates that the memory slot recall mode is active (default)
Memory_slot(fb)	A	Indicates the currently active memory slot, 255d = no active slot
Backlight_intensity(fb)	A	Indicates the current backlight intensity
Remote_control_on(fb)	D	Indicates that the infrared receiver is enabled
Remote_control_off(fb)	D	Indicates that the infrared receiver is disabled

PARAMETERS:

ID	The ID of display #1 (typically "A1", or Controller 'A', Display '1')
Memory_timeout	The amount of time the system takes to recall a memory slot. Typically 5-15sec. No commands will be sent to the matrix processor during this time.

TESTING:

OPS USED FOR TESTING:	3-Series: v1.007.0019
SIMPL WINDOWS USED FOR TESTING:	4.02.20
DEVICE DB USED FOR TESTING:	52.00.007.00
CRES DB USED FOR TESTING:	41.05.006.00
SYMBOL LIBRARY USED FOR TESTING:	
SAMPLE PROGRAM:	Planar Matrix MC3 v1.0.smw
REVISION HISTORY:	v1.0 – Original release.